

Application Notes

- 1. Supply, Clock, Data and Ground leads to be wired through the PS2-IS-BARRIER device as shown above.
- The PS2-IS-BARRIER relies upon a reliable low-resistance safety ground for proper functioning. Connect safety
 ground lead to grounding stake or bus bar per documents referenced in Notes 3 & 4.
- 3. Installation shall be in accordance with ISA RP 12.06.01
- 4. Installation shall be in accordance with the NEC, Article 504, ANSI/NFPA 70
- 5. Control equipment power must not exceed 250V
- The specified Lo and Co values are valid if one of the following conditions are met:
 6a. The total Li of the external circuit (excluding the cable) is < 1% of the Lo value or;
 6b. The total Ci of the external circuit (excluding the cable) is < 1% of the Co value.
- The values of Lo and Co shall be reduced to 50% of the specified values when both of the following conditions are met:
 - 7a. The total Li of the external circuit (excluding the cable) is >1% of the Lo value and
 - 7b. The total Ci of the external circuit (excluding the cable) is >1% of the Co value.
 - NOTE: the reduced capacitance of the external circuit (including cable) shall not be greater than 1uF for Groups C,D,E,F,G and 600nF for Groups A,B.
- 8. The PS2-IS-BARRIER is intended for interconnection with FM Approved intrinsically safe apparatus under the Entity concept where the following relationships are observed:

PS2-IS-BARRIER	Relationship	Intrinsically Safe Equipment
Uo	VI	Ui (or Vmax)
lo		li (or Imax)
Po	≤	Pi
Со	≥	Ci + interconnecting Ccable
Lo	≥	Li + interconnecting Lcable*

- Though the PS2-IS-BARRIER limits output energy to intrinsically safe levels, it does not provide galvanic isolation from earth or non-intrinsically safe circuits.
- 10. Installation Environment: The PS2-IS-BARRIER is intended for installation within a clean, dry, indoor, climate-controlled environment.

Intrinsic Safety Barrier Entity Parameters								
Uo (V)	lo (mA)	Po (mW)	Groups	Co (uF)	Lo (uH)	Lo/Ro (μH/Ω)		
7.71	790.0	1124	A,B	9.8	57	27.6		
			C,E,F,G	145	228	110.6		
			D	1000	456	221.2		

The above current and power values reflect parallel summation of all barrier output channels.

*Alternatively, Li may be greater than Lo and the cable length restrictions due to cable inductance (Lcable) can be ignored if both of the following conditions are met:

8a. Lo/Ro ≥ Li/Ri

8b. Lo/Ro ≥ Lcable/Rcable

No revision to drawing
w/o prior FM approval

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